

Improved Quad CMOS Analog Switches

FEATURES

- ±22-V Supply Voltage Rating
- CMOS Compatible Logic
- Low On-Resistance—$r_{DS(on)}$: 45 Ω
- Low Leakage—$I_{D(on)}$: 20 pA
- Single Supply Operation Possible
- Extended Temperature Range
- Fast Switching—t_{ON}: < 200 ns
- Low Glitching—Q: 1 pC

BENEFITS

- Wide Analog Signal Range
- Simple Logic Interface
- Higher Accuracy
- Minimum Transients
- Reduced Power Consumption
- Superior to DG308A/309
- Space Savings (TSSOP)

APPLICATIONS

- Industrial Instrumentation
- Test Equipment
- Communications Systems
- Disk Drives
- Computer Peripherals
- Portable Instruments
- Sample-and-Hold Circuits

DESCRIPTION

The DG308B/309B analog switches are highly improved versions of the industry-standard DG308A/309. These devices are fabricated in Vishay Siliconix' proprietary silicon gate CMOS process, resulting in lower on-resistance, lower leakage, higher speed, and lower power consumption.

These quad single-pole single-throw switches are designed for a wide variety of applications in telecommunications, instrumentation, process control, computer peripherals, etc. An improved charge injection compensation design minimizes switching transients. The DG308B and DG309B can handle up to ±22-V input signals. An epitaxial layer prevents latchup.

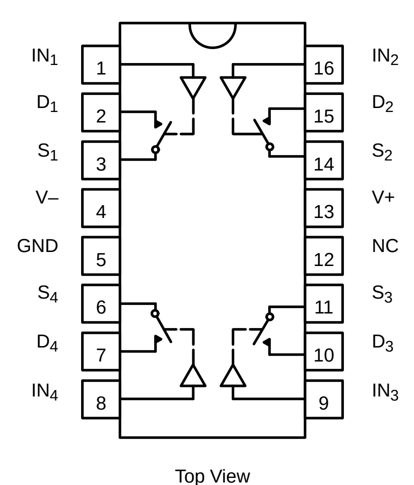
All devices feature true bi-directional performance in the on condition, and will block signals to the supply levels in the off condition.

The DG308B is a normally open switch and the DG309B is a normally closed switch. (See Truth Table.)

FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION

DG308B

Dual-In-Line, SOIC and TSSOP



Top View

TRUTH TABLE

Logic	DG308B	DG309B
0	OFF	ON
1	ON	OFF

Logic "0" ≤ 3.5V
Logic "1" ≥ 11 V

ORDERING INFORMATION

Temp Range	Package	Part Number
–40 to 85°C	16-Pin Plastic DIP	DG308BDJ
		DG309BDJ
	16-Pin Narrow SOIC	DG308BDY
		DG309BDY
	16-Pin TSSOP	DG308BDQ
		DG309BDQ
–55 to 125°C	16-Pin CerDIP	DG308BAK
		DG308BAK/883
		DG309BAK
		DG309BAK/883



ABSOLUTE MAXIMUM RATINGS

Voltages Referenced to V–

V+ . . . . 44 V

GND . . . . 25 V

Digital Inputs[a] V_S, V_D . . . . (V–) –2 V to (V+) +2 V or 30 mA, whichever occurs first

Current, Any Terminal . . . . 30 mA

Peak Current, S or D (Pulsed at 1 ms, 10% duty cycle max) . . . . 100 mA

Storage Temperature (AK, Suffix) . . . . –65 to 150°C

(DJ, DY, DQ Suffix) . . . . –65 to 125°C

Power Dissipation (Package)[b]

16-Pin Plastic DIP[c] . . . . 470 mW

16-Pin Narrow SOIC and TSSOP[d] . . . . 640 mW

16-Pin CerDIP[e] . . . . 900 mW

Notes:

a. Signals on S_X, D_X, or IN_X exceeding V+ or V– will be clamped by internal diodes. Limit forward diode current to maximum current ratings.
b. All leads welded or soldered to PC Board.
c. Derate 6.5 mW/°C above 75°C
d. Derate 7.6 mW/°C above 75°C
e. Derate 12 mW/°C above 75°C

SPECIFICATIONS[a]

Parameter	Symbol	Test Conditions Unless Specified V+ = 15 V, V– = –15 V V_{IN} = 11 V, 3.5 V[f]	Temp[b]	Typ[c]	A Suffix –55 to 125°C Min[d]	A Suffix Max[d]	D Suffix –40 to 85°C Min[d]	D Suffix Max[d]	Unit
Analog Switch									
Analog Signal Range[e]	V_{ANALOG}		Full		−15	15	−15	15	V
Drain-Source On-Resistance	$r_{DS(on)}$	V_D = ±10 V, I_S = 1 mA	Room Full	45		85 100		85 100	Ω
$r_{DS(on)}$ Match	$\Delta r_{DS(on)}$		Room	2					%
Source Off Leakage Current	$I_{S(off)}$	V_S = ±14 V, V_D = ∓14 V	Room Full	±0.01	−0.5 −20	0.5 20	−0.5 –5	0.5 5	nA
Drain Off Leakage Current	$I_{D(off)}$	V_D = ±14 V, V_S = ∓14 V	Room Full	±0.01	−0.5 −20	0.5 20	−0.5 –5	0.5 5	
Drain On Leakage Current	$I_{D(on)}$	V_S = V_D = ±14 V	Room Full	±0.02	−0.5 −40	0.5 40	–0.5 –10	0.5 10	
Digital Control									
Input Voltage High	V_{INH}		Full		11		11		V
Input Voltage Low	V_{INL}		Full			3.5		3.5	
Input Current	I_{INH} or I_{INL}	V_{INH} or V_{INL}	Full		–1	1	–1	1	µA
Input Capacitance	C_{IN}		Room	5					pF
Dynamic Characteristics									
Turn-On Time	t_{ON}	V_S = 3 V, See Figure 2	Room			200		200	ns
Turn-Off Time	t_{OFF}		Room			150		150	
Charge Injection	Q	C_L = 1000 pF, V_g= 0 V, R_g = 0 Ω	Room	1					pC
Source-Off Capacitance	$C_{S(off)}$	V_S = 0 V, f = 1 MHz	Room	5					pF
Drain-Off Capacitance	$C_{D(off)}$		Room	5					
Channel On Capacitance	$C_{D(on)}$	V_D = V_S = 0 V, f = 1 MHz	Room	16					
Off Isolation	OIRR	C_L = 15 pF, R_L = 50 Ω V_S = 1 V_{RMS}, f = 100 kHz	Room	90					dB
Channel-to-Channel Crosstalk	X_{TALK}		Room	95					





SPECIFICATIONS[a]

Parameter	Symbol	Test Conditions Unless Specified V+ = 15 V, V– = –15 V V_{IN} = 11 V, 3.5 V[f]	Temp[b]	Typ[c]	A Suffix –55 to 125°C Min[d]	A Suffix Max[d]	D Suffix –40 to 85°C Min[d]	D Suffix Max[d]	Unit
Power Supply									
Positive Supply Current	I+	V_{IN} = 0 or 15 V	Room Full			1 5		1 5	µA
Negative Supply Current	I–		Room Full		−1 −5		−1 −5		
Power Supply Range for Continuous Operation	V_{OP}		Full		±4	±22	±4	±22	V

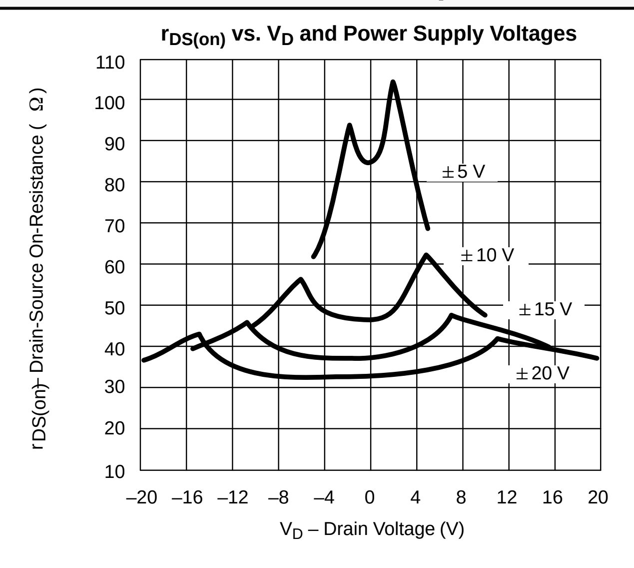
SPECIFICATIONS[a] FOR SINGLE SUPPLY

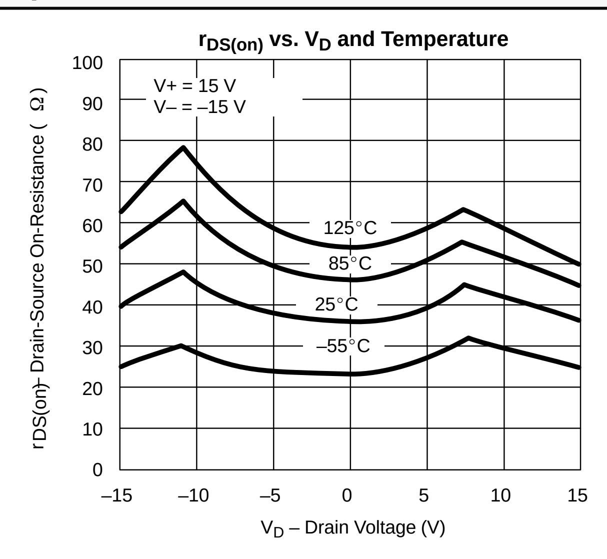
Parameter	Symbol	Test Conditions Unless Specified V+ = 12 V, V– = 0 V V_{IN} = 11 V, 3.5 V[f]	Temp[b]	Typ[c]	A Suffix –55 to 125°C Min[d]	A Suffix Max[d]	D Suffix –40 to 85°C Min[d]	D Suffix Max[d]	Unit
Analog Switch									
Analog Signal Range[e]	V_{ANALOG}		Full		0	12	0	12	V
Drain-Source On-Resistance	$r_{DS(on)}$	V_D = 3 V, 8 V, I_S = 1 mA	Room Full	90		160 200		160 200	Ω
Dynamic Characteristics									
Turn-On Time	t_{ON}	V_S = 8 V, See Figure 2	Room			300		300	ns
Turn-Off Time	t_{OFF}		Room			200		200	
Charge Injection	Q	C_L = 1 nF, V_{gen}= 6 V, R_{gen} = 0 Ω	Room	4					pC
Power Supply									
Positive Supply Current	I+	V_{IN} = 0 or 12 V	Room Full			1 5		1 5	µA
Negative Supply Current	I–		Room Full		−1 −5		−1 −5		
Power Supply Range for Continuous Operation	V_{OP}		Full		4	44	4	44	V

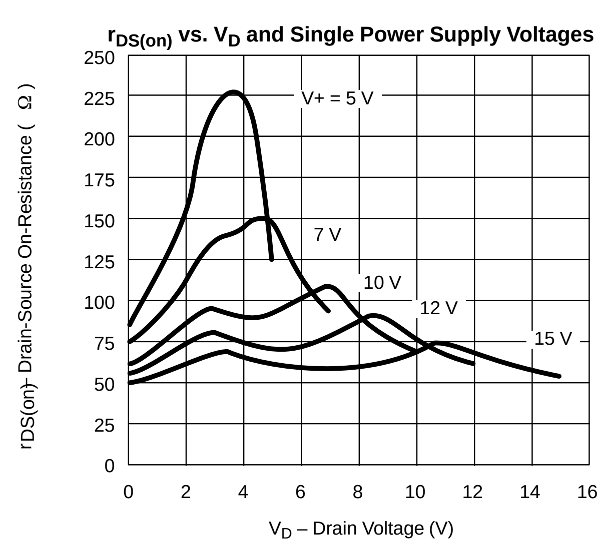
Notes:
a. Refer to PROCESS OPTION FLOWCHART.
b. Room = 25°C, Full = as determined by the operating temperature suffix.
c. Typical values are for DESIGN AID ONLY, not guaranteed nor subject to production testing.
d. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet.
e. Guaranteed by design, not subject to production test.
f. V_{IN} = input voltage to perform proper function.

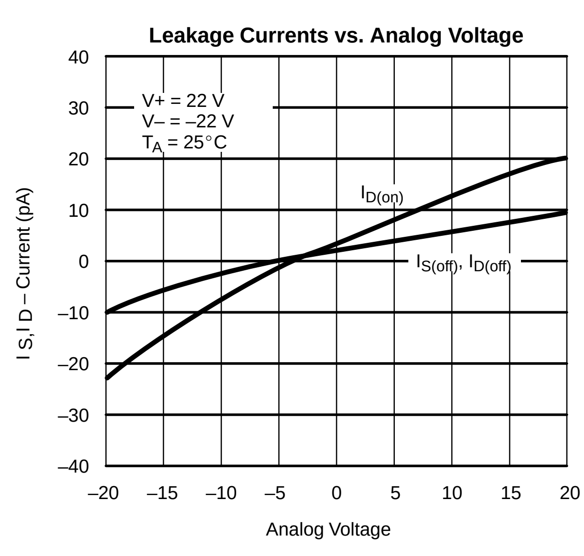


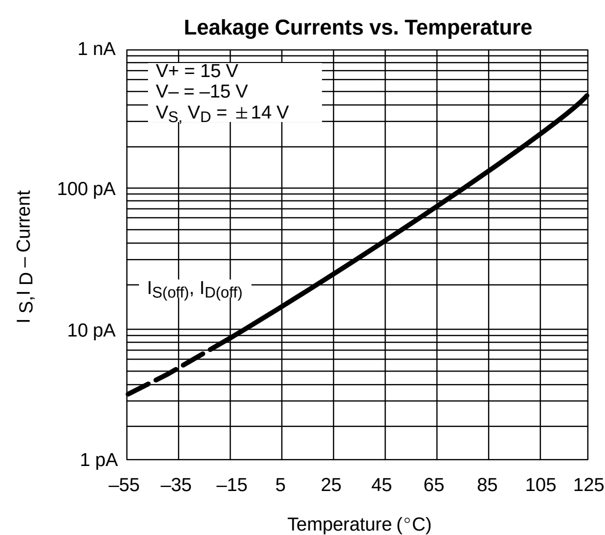
TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

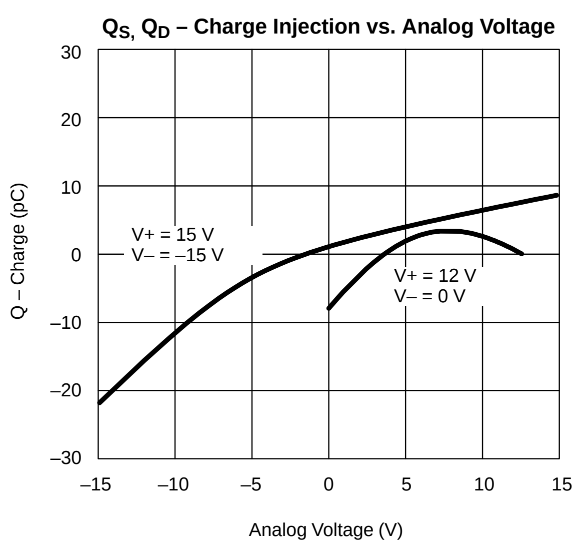




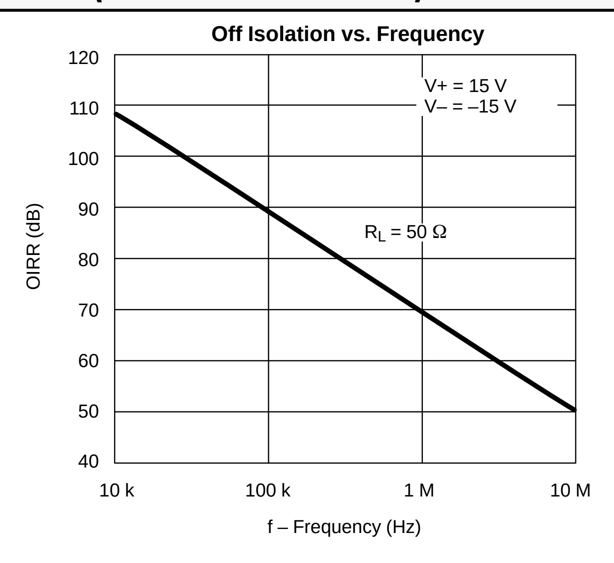








TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)



SCHEMATIC DIAGRAM (TYPICAL CHANNEL)

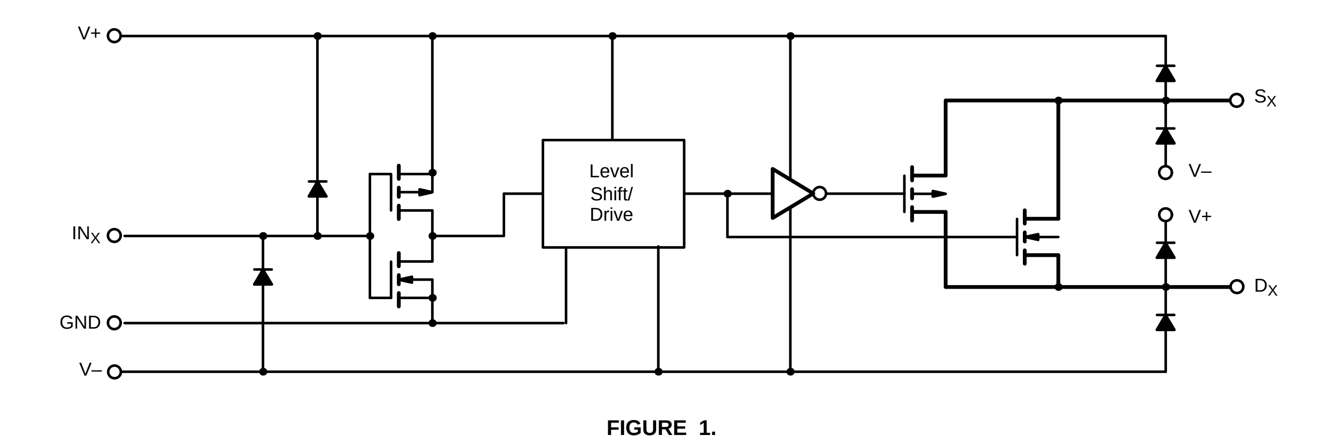


FIGURE 1.

TEST CIRCUITS

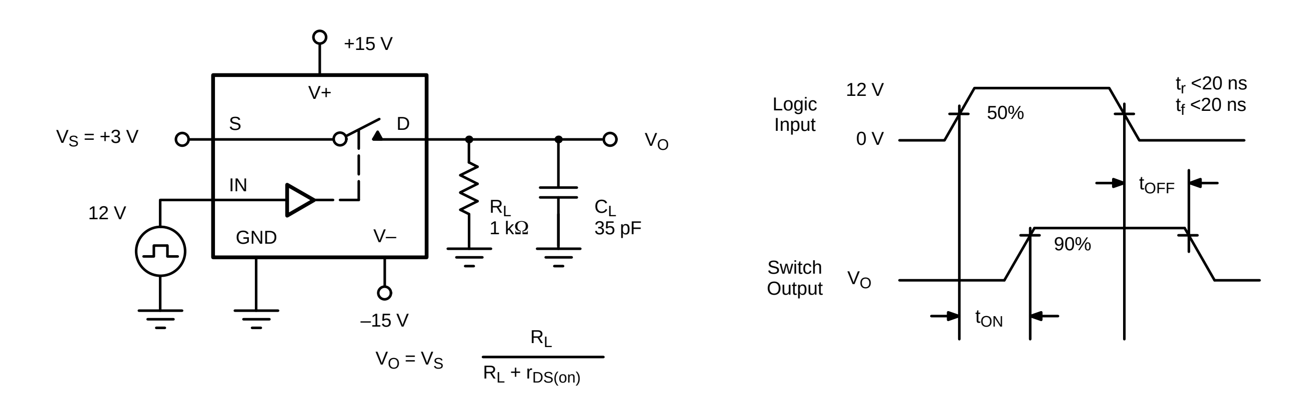


FIGURE 2. Switching Time



TEST CIRCUITS

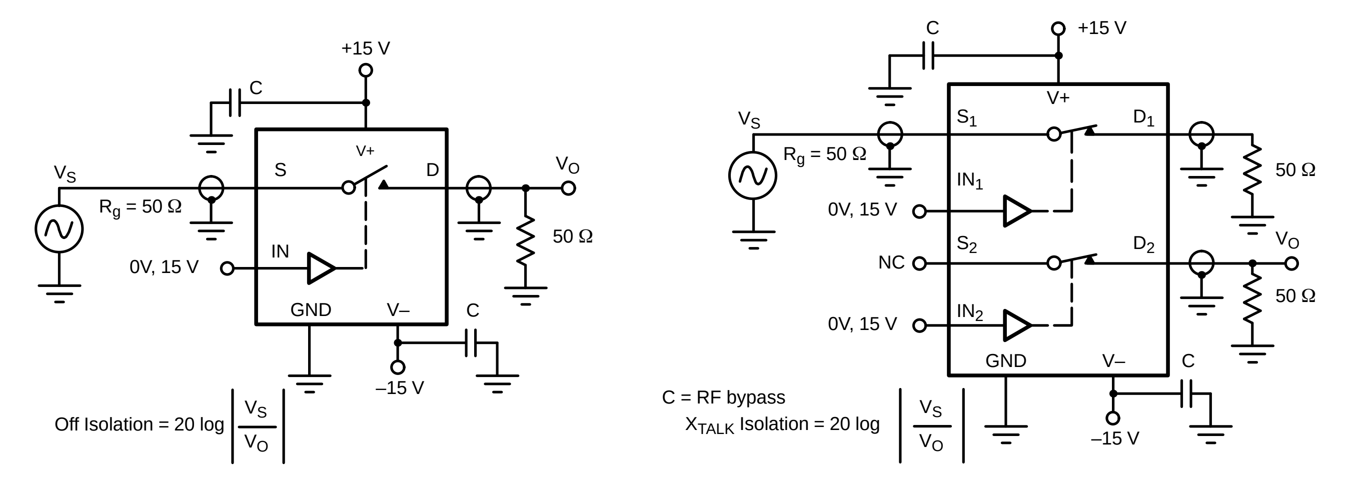
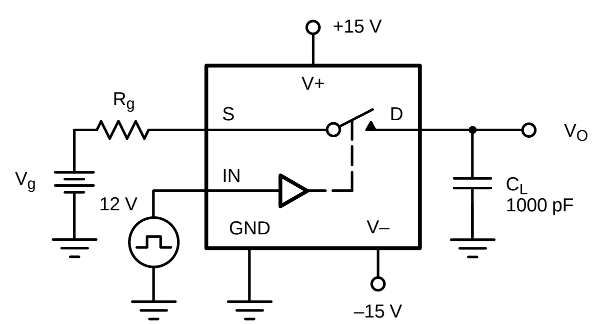
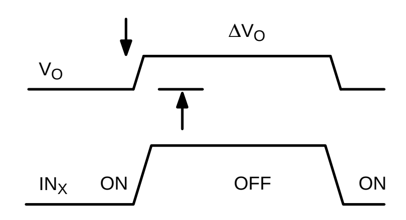


FIGURE 3. Off Isolation

FIGURE 4. Channel-to-Channel Crosstalk





ΔV_O = measured voltage error due to charge injection
The charge injection in coulombs is $Q = C_L \times \Delta V_O$

FIGURE 5. Charge Injection

APPLICATIONS

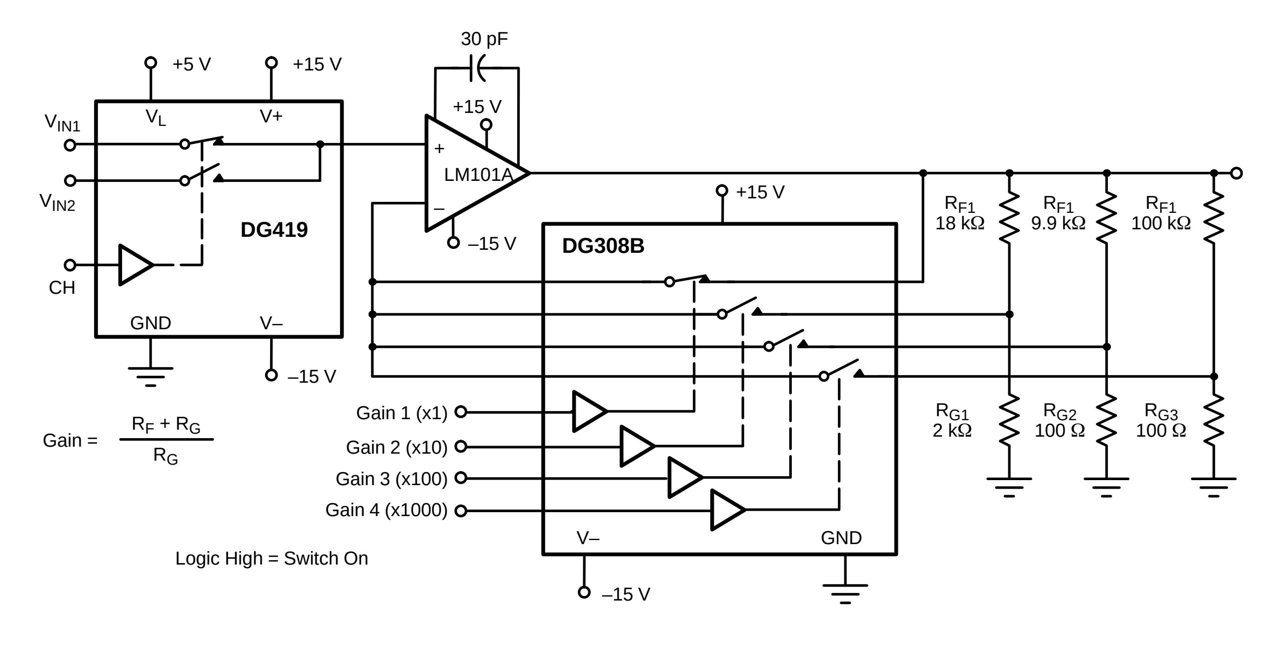


FIGURE 6. A Precision Amplifier with Digitally Programmable Inputs and Gains



APPLICATIONS

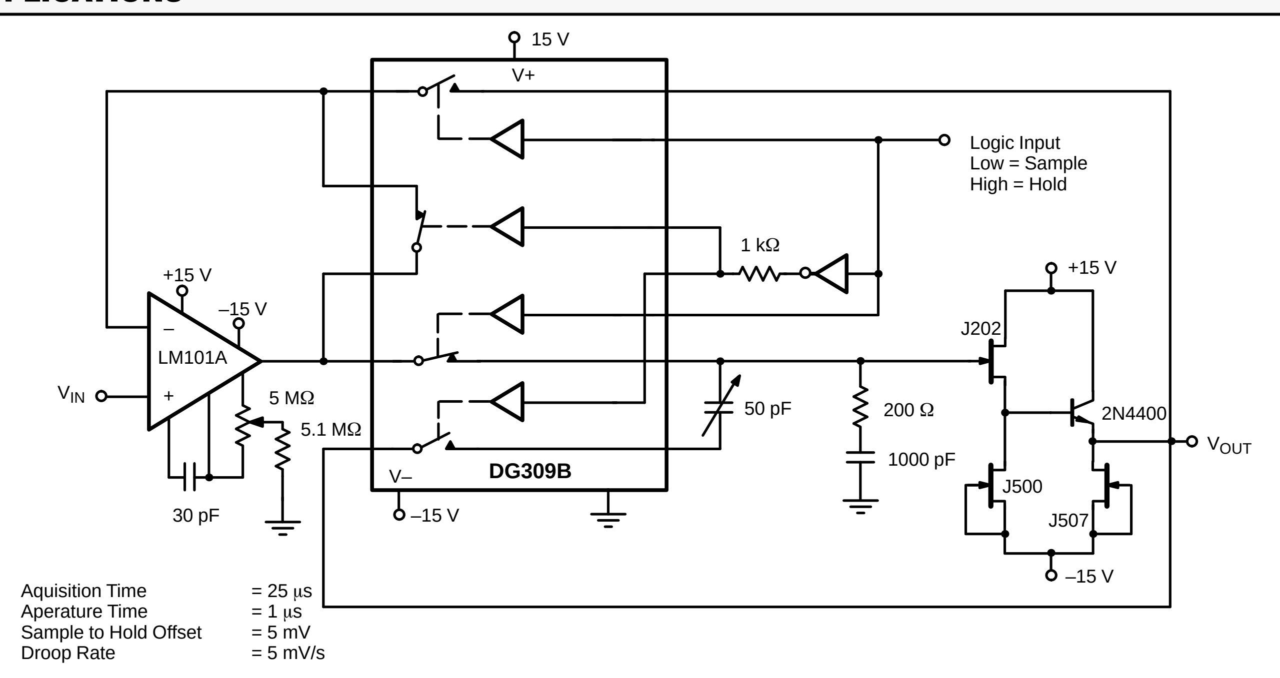


FIGURE 7. Sample-and-Hold

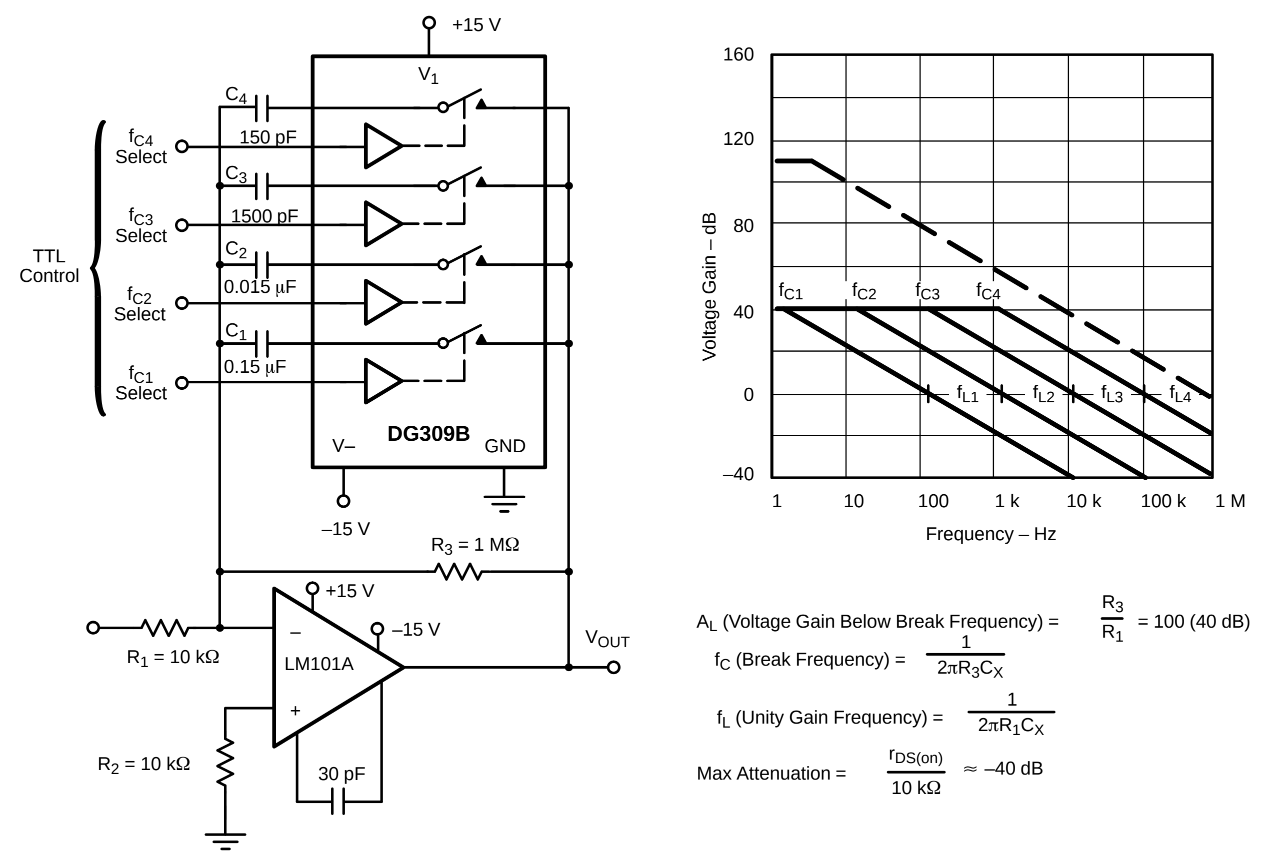


FIGURE 8. Active Low Pass Filter with Digitally Selected Break Frequency